SINGLE TURN ABSOLUTE ENCODER 390

Absolute single turn encoder, from 2 to 14 bits, in metallic, strong, oil/dust/moisture proof container, for application in heavy-duty, adverse environmental conditions operations.

Absolute single turn page: 01.390 $1 / 2$ data sheet IA020 E00


Measures without tolerance according to UNI ISO 2768-mk


* 73 for code different from Gray code.


## TECHNICAL FEATURES AND POSSIBLE CONFIGURATIONSI



| P O S I B L E P T O N |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| CODE | DESCRIPTION | CODE | DESCRIPTION | CODE | DESCRIPTION |
| $\begin{aligned} & \text { A } \\ & \text { L } \\ & \text { Y } \end{aligned}$ | High temperature <br> Low temperature <br> Unbreakable disk | $\begin{aligned} & \mathrm{X} \\ & \mathrm{H} \\ & \mathrm{P} \end{aligned}$ | Custom options <br> Different shaft $\varnothing$ <br> Compressed air attack | $\begin{aligned} & \mathrm{G} \\ & \mathrm{Z} \\ & \mathrm{~B} \end{aligned}$ | Tropicalization <br> Sealed bal bearing <br> Low torque ball bear. |
|  | cos |  |  |  |  |


| ELECTRONIC OUTPUT INTERFACE | PARALLEL OPTIONS |
| :--- | :--- | :--- |

» (-) NPN open coll.10mA(std)

» (KK) NPN open coll. $70 \mathrm{~mA}+\mathrm{PTC}$

» (KP) NPN open emitt. $70 \mathrm{~mA}+\mathrm{PTC}$

» (B) Push-p. 70mA+PTC 1 $\div 12$ bit


## » (-) Parallel (standard)

The angular value is present on the output connector in a 'parallel' form. The number of conductors is proportional to the resolution and the type of selected code. Any electronic output stage can be applied.

## » (S) Serial SSI

The data output (available only with Gray or Excess code) is controlled by an external clock. The angular position's value is first stored into the encoder and then transmitted outside starting from the MSB. The standard of transmission is RS422.
» (A) Analogic 'current'
» (V) Analogic 'voltage'
Encoder's angular position with 10 bits resolution (1024 div./rev) is converted in an analog ( mA or V ) quantity proportional to its value. In the standard version $\left(360^{\circ}\right.$ of rotation) $\mathrm{Iu}=1 \div 20 \mathrm{~mA} \mathrm{Vu}=0 \div 10 \mathrm{~V}$.

## " (F) Led zero

Designed for setting the encoder position with mechanical position. Led lights up at zero value with clockwise increasing code, at max. value with contrary clockwise.
» (L) Latch
When Latch input is set to 0 Vdc the outputs at Gray code disc level are 'frozen'; in such a way detections are carried out in stable conditions. For this reason Latch input is standard for Binary and BCD codes.
(P) Parity

An electronic circuit generates a signal (Parity) in such a way that the number of high level outputs is always odd; then there isn't any condition with all low level outputs. This option allows to carry out automatic checkout of the output stages.

## » (M) Multiplexer

The outputs must be enabled by an external 'enable' signal; this method provides the connection in parallel of several encoders on a single bus. The number of conductors is proportional to the resolution and to the type of selected code.

## ORDERING CODE


(-) Parallel (standard)
(S) Serial SSI
(B) Binary
(D) $B C D$
(A) Analogic 'current'
( CV ) 1 m long cable IP65 ( CM5 ) Conn. 26 pin PT02A16-26P IP65 ( SM5 ) Conn. 26 pin PT02E16-26P IP66 ( CM3 ) Conn. 23 pin PT02A16-23P IP65 ( SM3 ) Conn. 23 pin PT02E16-23P IP66 ( CL ) Conn. 10 pin MS310A18-1P IP65 ( SL ) Conn. 10 pin MS310R18-1P IP66 ( CH ) Conn. 12 pin Eml 121 IP65 ( CD4) Conn. 25 pin DB25P IP64
(-) NPN open coll.10mA(std)
(KK) NPN open coll. $70 \mathrm{~mA}+\mathrm{PTC}$
(KP) NPN open emitt. $70 \mathrm{~mA}+\mathrm{PTC}$
(B) Push-p. $70 \mathrm{~mA}+\mathrm{PTC} 1 \div 12 \mathrm{bit}$

Lonly for parallel interface


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